L Number	Hits	Search Text	DB	Time stamp
33	1	(20020040459.pn.) and frequency and transfer and clock and parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 14:05
34	318	clock with frequency with transfer with rate with signal$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 14:06
35	93	(clock with frequency with transfer with rate with signal$1) and (integrated adj circuit$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 15:36
-	2	2000298525.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 15:20
-	1129	714/738.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 15:28
-	0	(714/738.ccls.) and (integrated adj circuit) and semiconductor and test$3 and (trasmit$4 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 15:33
-	0	(714/738.ccls.) and (integrated adj circuit$1) and semiconductor and test$3 and (trasmi$4 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 15:33
-	0	(integrated adj circuit$1) and semiconductor and test$3 and (trasmi$4 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 15:33
-	2	(714/738.ccls.) and (integrated adj circuit$1) and semiconductor and test$3 and (transmi$5 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:10
-	118	(integrated adj circuit$1) and semiconductor and test$3 and (transmi$5 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:11
-	51	(integrated adj circuit$1) and semiconductor and test$3 and (transmi$5 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5 and (test$3 adj mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:14
-	14	(integrated adj circuit$1) and semiconductor and test$3 and (transmi$5 with parallel with serial) and (receiv$3 with serial with parallel) and (test$3 with generat$3) and compar$5 and (test$3 adj mode) and (loopback)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 16:20
-	17	(integrated adj circuit$1) and semiconductor and test$3 and (transmi$5 same parallel same serial) and (receiv$3 same serial same parallel) and (test$3 with generat$3) and compar$5 and (test$3 adj mode) and (loopback)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:36

–	406	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:38
–	151	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected with pulse)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:39
–	48	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:40
–	17	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit) and (signal with bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:40
–	17	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit) and (signal with bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:54
–	1	((hold$3 or stor$3) pulse$1 with circuit$1) and ((generat$3 adj expected) with pulse$1) and (integrated adj circuit) and (signal with bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:45
–	4	((hold$3 or stor$3) with pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 09:59
–	0	((hold$3 or stor$3) adj pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:57
–	0	((hold$3 or stor$3) adj pulse with circuit$1) and (generat$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:58
–	0	(((hold$3 or stor$3) adj pulse) with circuit$1) and (generat$3 adj pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:58
–	0	(((hold$3 or stor$3) adj pulse$1) with circuit$1) and (generat$3 adj pulse$1) and (integrated adj circuit) and (signal with bits) and (pulse$1 adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 17:59
–	1	(((hold$3 or stor$3) adj pulse$1) with circuit$1) and (integrated adj circuit) and (signal with bits) and (pulse$1 adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/25 18:01
–	2	20020040459.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 09:51
–	2	((hold$3) with pulse with circuit$1) and (generat$3 with expected with pulse) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:00

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-	11	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:04
-	9	((hold$3) with pulse with circuit$1) and (generat$3 with pulse$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:11
-	0	((hold$3) with pulse with circuit$1) and (generat$3 with pulse$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) and flipflop	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:12
-	2	((hold$3) with pulse with circuit$1) and (generat$3 with pulse$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) and(flip adj flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:13
-	2	((hold$3) with pulse with circuit$1) and (generat$3 with pulse$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) and (flip adj flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:14
-	3	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (signal with bits) and (pulse adj sequence) and (flip adj flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:48
-	85	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (signal with bits) and ((flip adj flop) with circuits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:49
-	8	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (signal with bits) and ((flip adj flop) with circuits) and (((EX adj "OR") or (exclusive adj "or")) with circuits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:59
-	26	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (compar$5 with circuit$1) and (generat$3 with expected) and (compar$5 with pulse$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 11:43
-	2	((hold$3) with pulse with circuit$1) and (integrated adj circuit) and (compar$5 with circuit$1) and (generat$3 adj expected) and (compar$5 with pulse$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 11:03
-	1	(5787114.pn.) and mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:26
-	2	5043931.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:29
-	2	5111451.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:32
-	0	53434611.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:32

–	2	5343611.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:32
–	0	(BARTON.IN) and (loop adj back)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:34
–	0	BARTON.IN	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:34
–	4550	BARTON.IN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:34
–	7	(BARTON.IN.) and (loop adj back)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:46
–	6	(BARTON.IN.) and (loop adj back) and switch$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 12:58
–	260	(switch$3 with input with (test adj signal)) and (integrated adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 17:17
–	27	(compar$5 with circuit$1) same ("AND" with circuit$1) same (NOR with circuit$1) same (flip adj flop) same latch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:59

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